



QUANTUM COMPUTING AT JSC

JUNIQ - JÜLICH UNIFIED INFRASTRUCTURE FOR QUANTUM COMPUTING

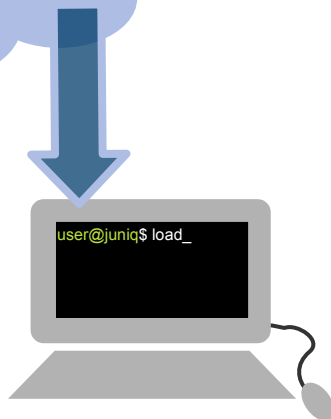
22 MARCH 2023 | MADITA WILLSCH

JUNIQ: The Vision

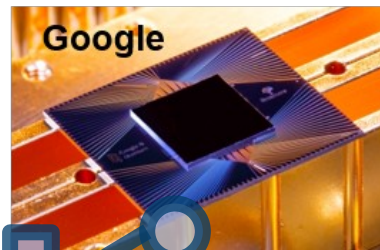


HPC systems

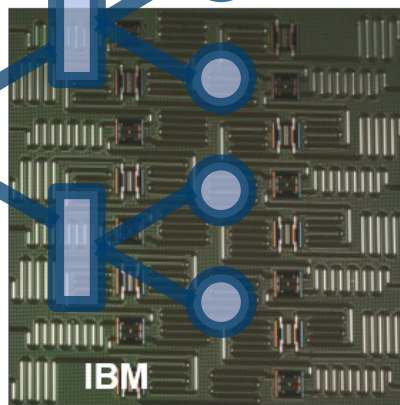
JUNIQ-Cloud



Member of the Helmholtz Association



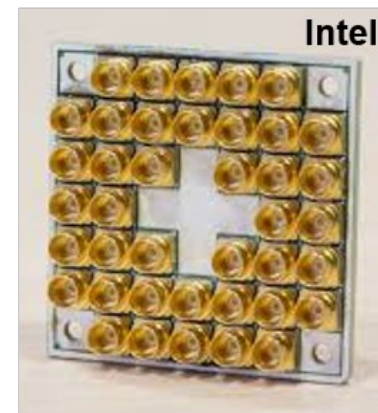
Google



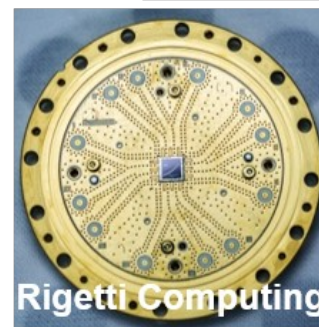
IBM



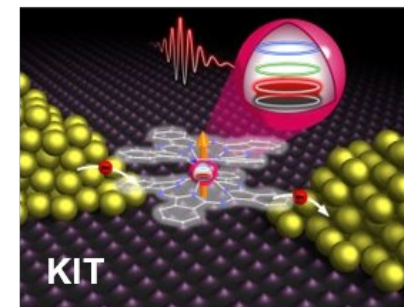
D-Wave



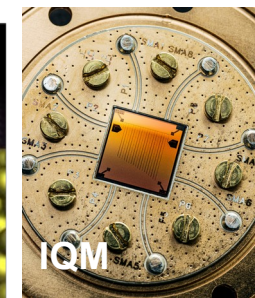
Intel



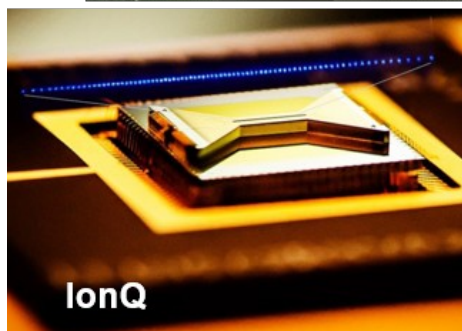
Rigetti Computing



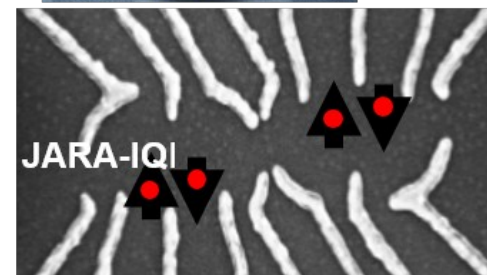
KIT



IQM



IonQ



JARA-IQI



PASQAL

Quantum computers and simulators (QCS)

Hybrid quantum-classical computing systems

for the realization of the full potential of quantum computing

- **Hybrid quantum-classical algorithms**

- Apply parameterized transformations to quantum system, evaluate energy expectation value → classical optimization algorithm suggests new parameters
 - Variational Quantum Eigensolver – **VQE**: quantum chemistry
 - Quantum Approximate Optimization Algorithm – **QAOA**: optimization
- Classic workflows with potential quantum content

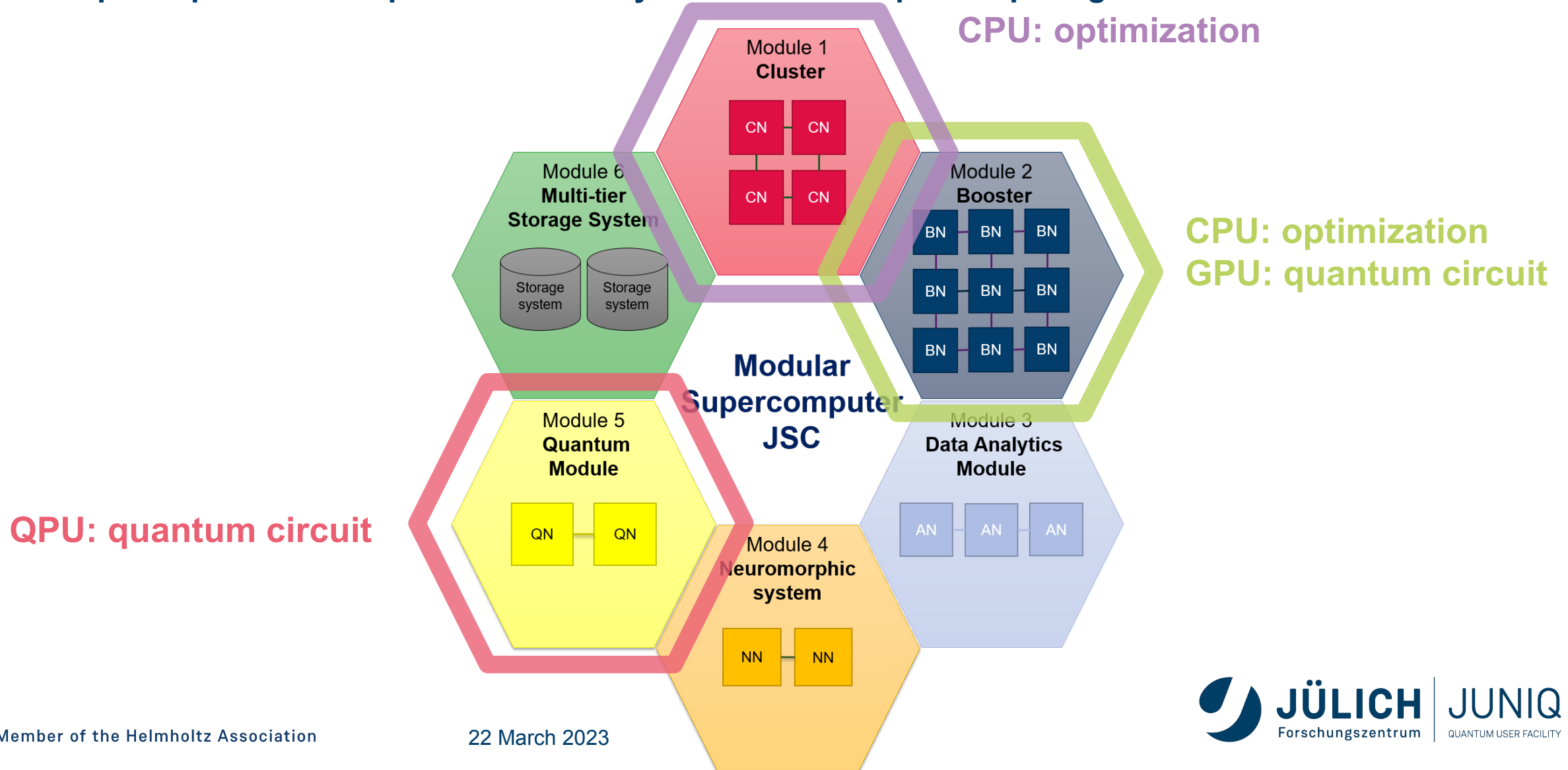
- **Hybrid quantum-classical hardware**



Latencies & execution times

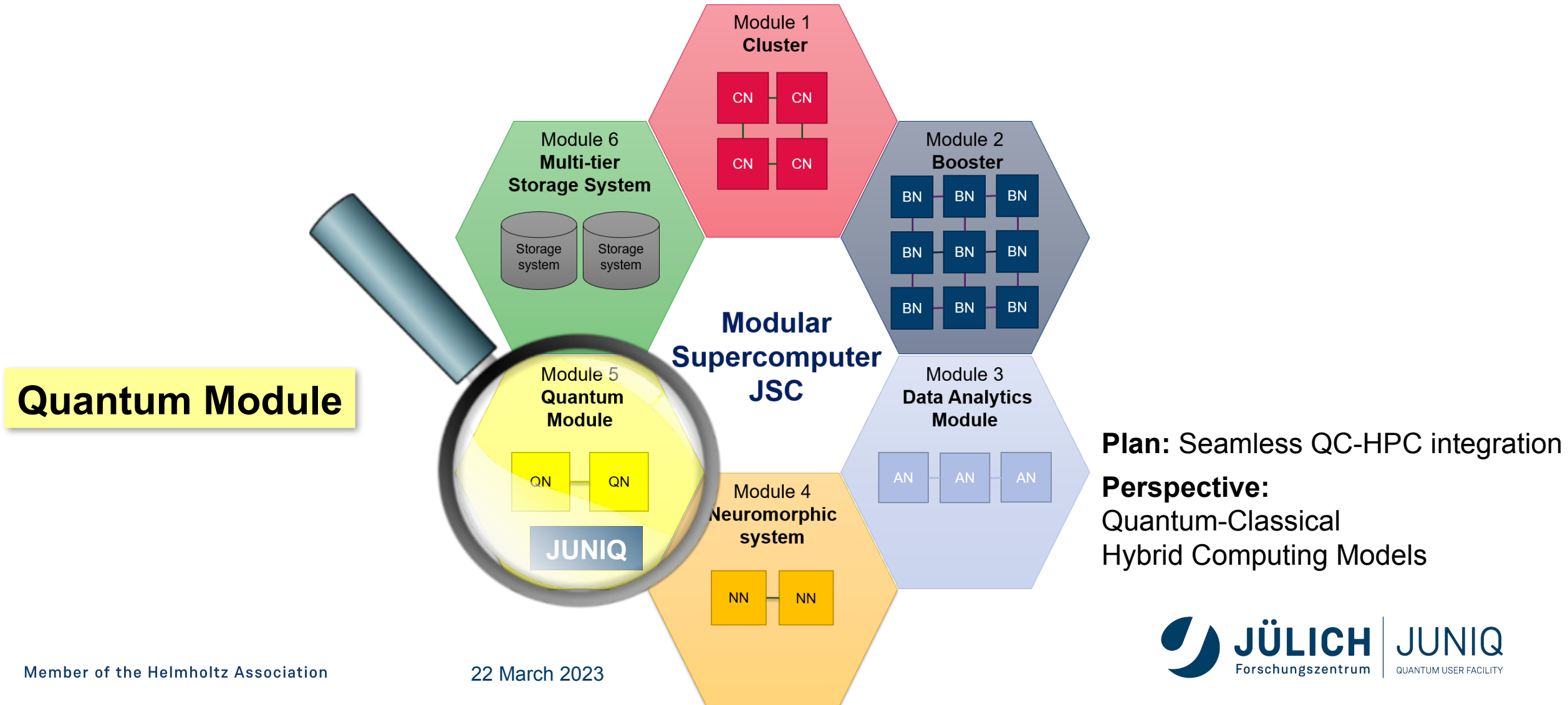
JUNIQ in the Modular Supercomputing Architecture

A European quantum computer user facility at the Jülich Supercomputing Centre



JUNIQ in the Modular Supercomputing Architecture

A European quantum computer user facility at the Jülich Supercomputing Centre



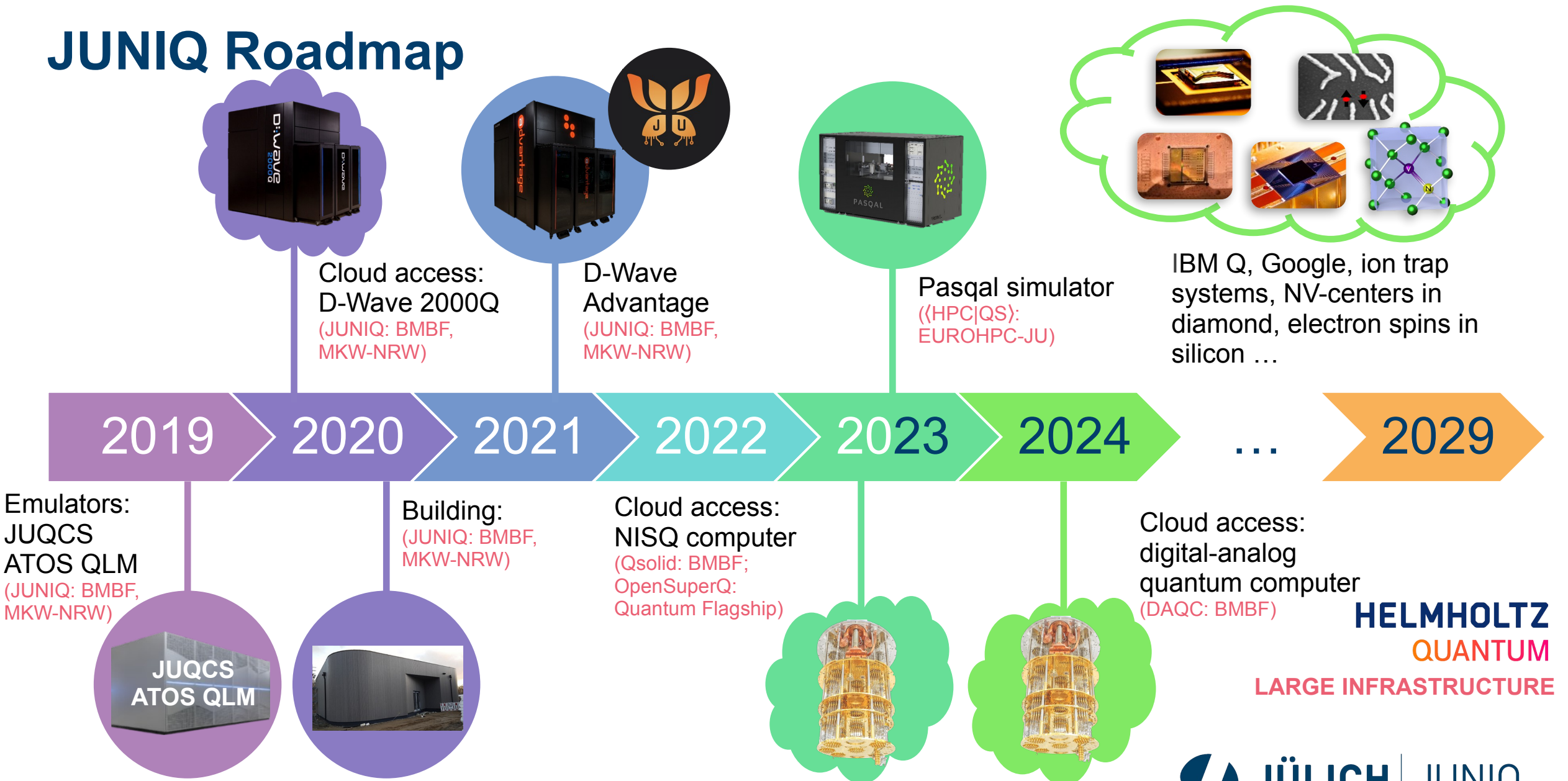
JUNIQ in the Modular Supercomputing Architecture



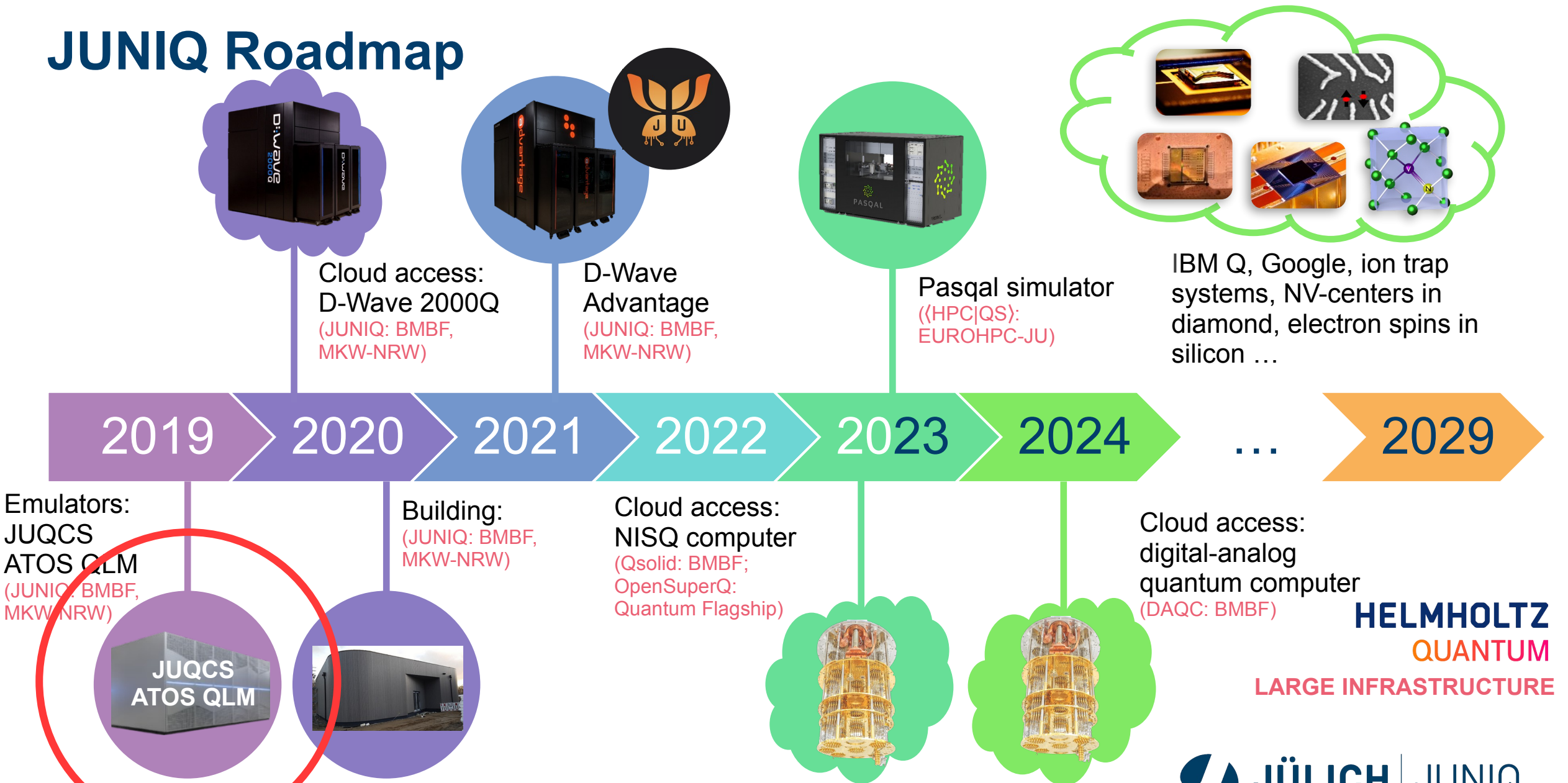
Quantum Computer User Facility:

- QC user facility for science and industry
- Installation, operation and provision of QCs
- Unified portal for access to QC emulators and to QC devices at different levels of technological maturity, based on different functionalities (QC-PaaS)
- Development and benchmarking of algorithms and prototype applications
- Services, training and user support
- Modular quantum-HPC hybrid computing

JUNIQ Roadmap



JUNIQ Roadmap



Jülich Universal Quantum Computer Simulator – JUQCS

- **What?**

- Emulation of gate-based quantum computers

- **How?**

- State-vector simulation $\rightarrow$ memory requirement is 2^N
- Gate-applications (matrix multiplication) performed by 2- or 4-component updates
- Efficient MPI-communication scheme
- GPU version available

- **What for?**

- Benchmarking of real devices and quantum algorithms
- Verification of (implementations of) quantum algorithms



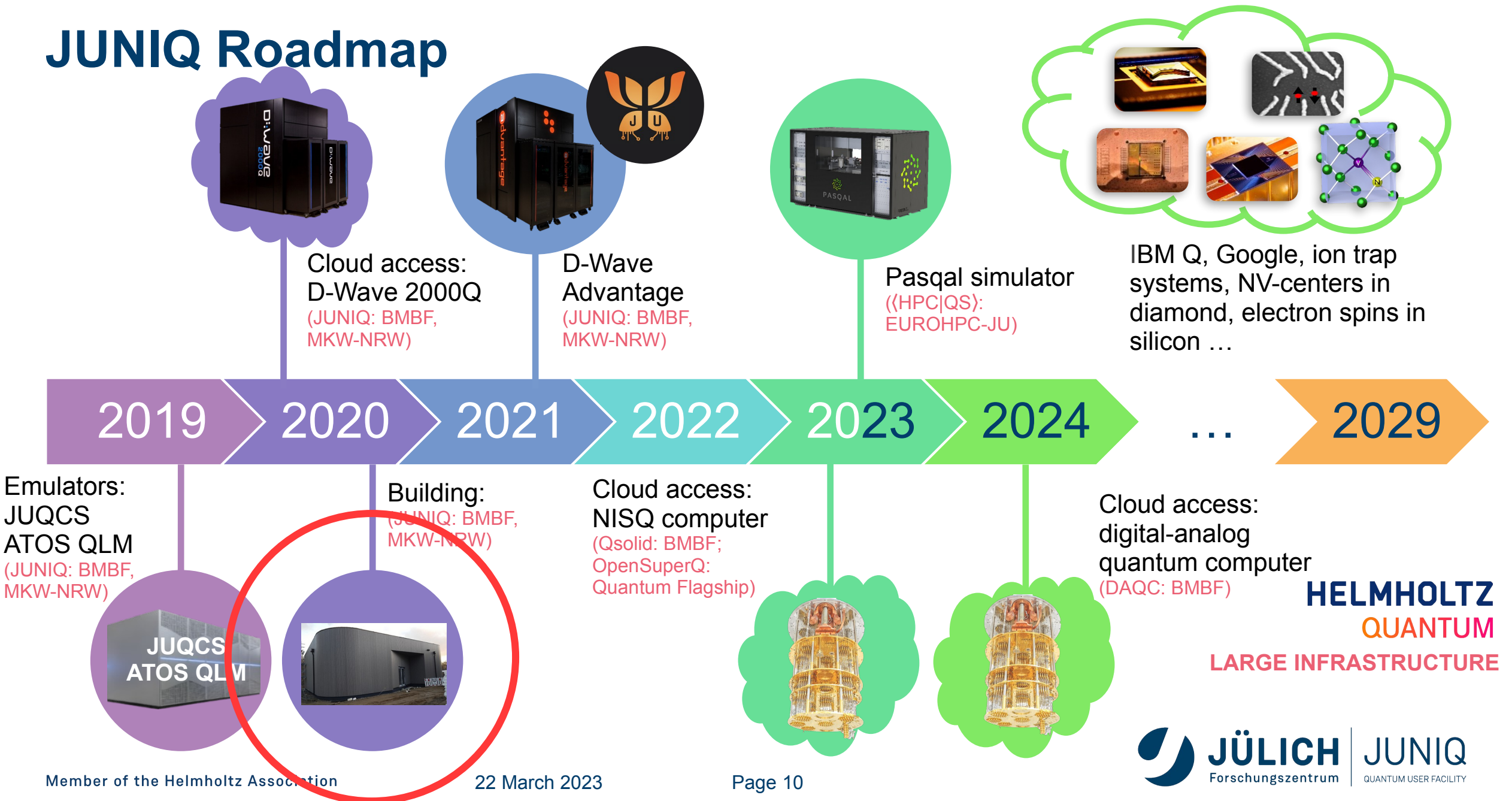
JUQCS

De Raedt et al., CPC **176**, 121 (2007)

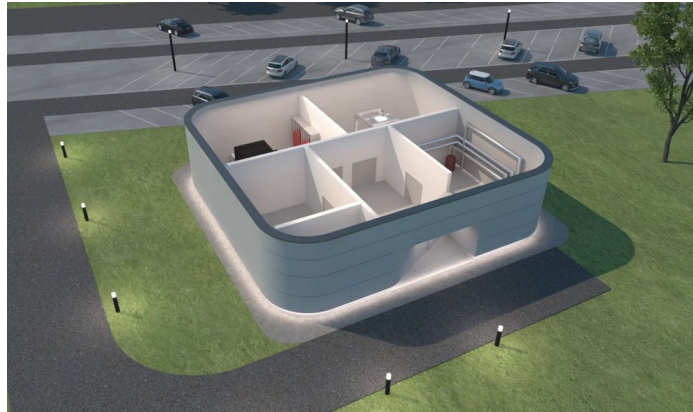
De Raedt et al., CPC **237**, 47 (2019)

Willsch et al., CPC **278**, 108411 (2022)

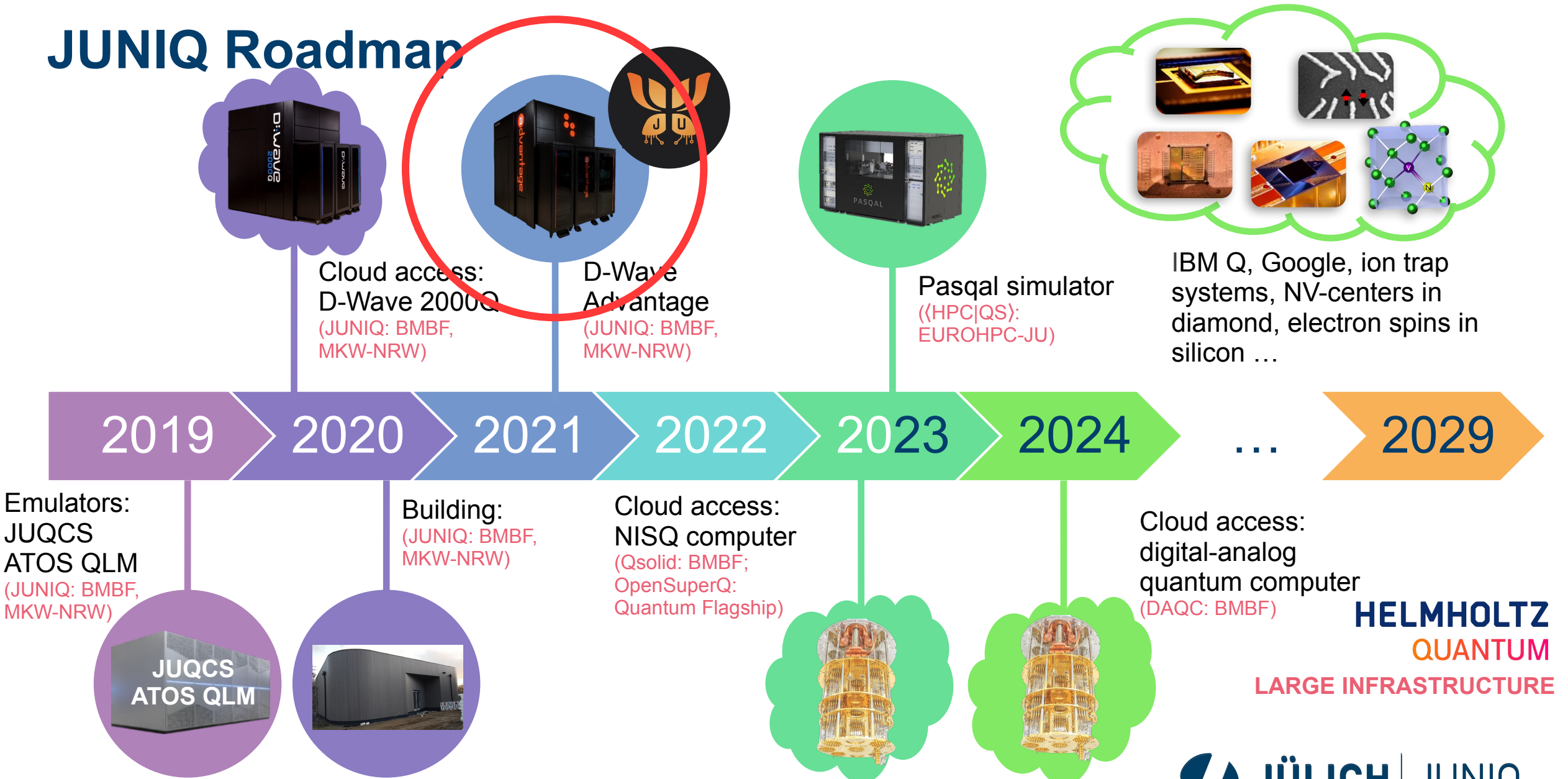
JUNIQ Roadmap



The JUNIQ Building



JUNIQ Roadmap



Quantum Annealers

What do they do?



Quadratic Unconstrained
Binary Optimization

QUBO: $\min_{x_i=0,1} \left(\sum_i a_i x_i + \sum_{i<j} b_{ij} x_i x_j \right)$

“bias”
(real number)

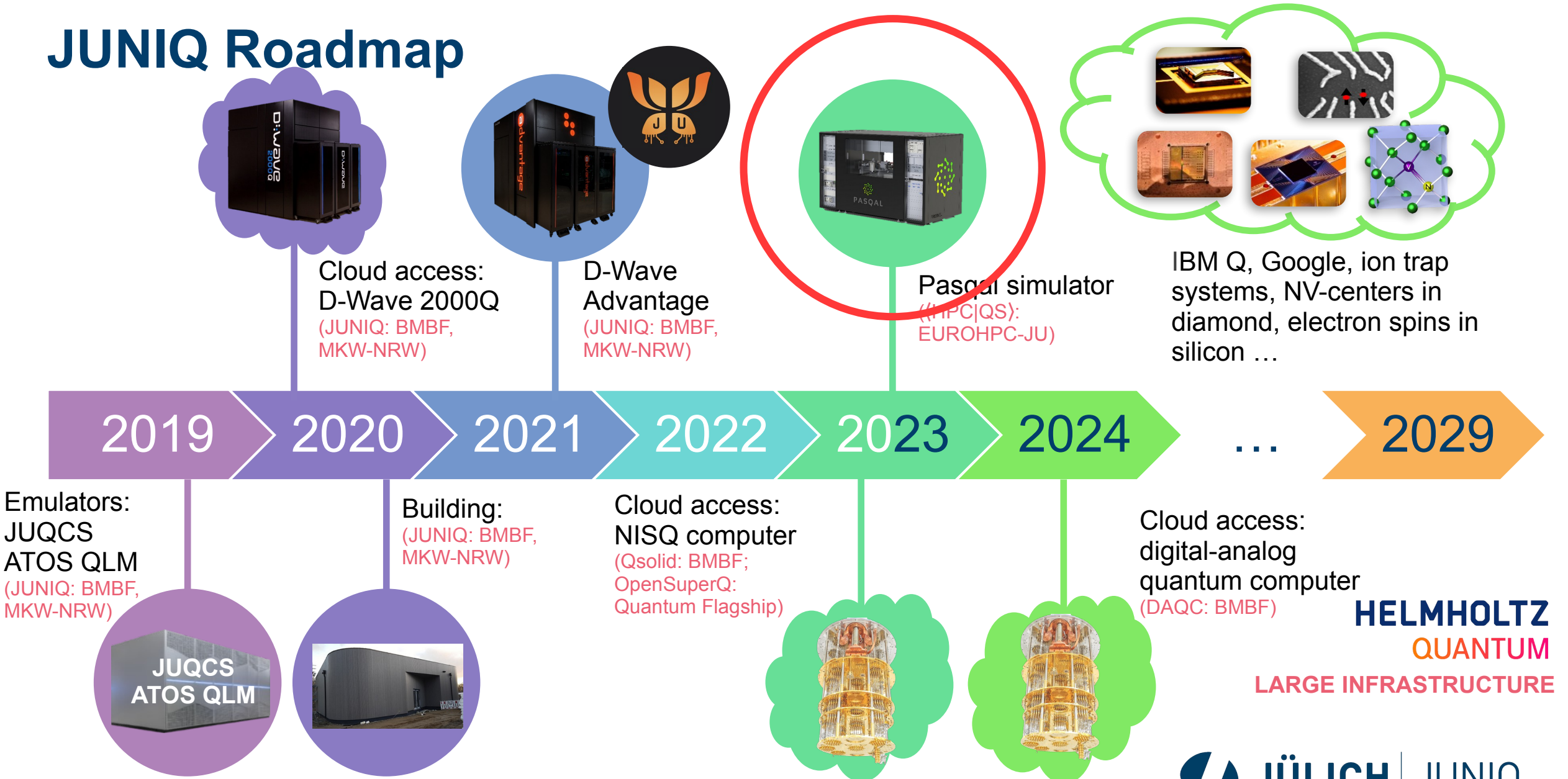
problem
variables
(binary)

“coupler”
(real number)

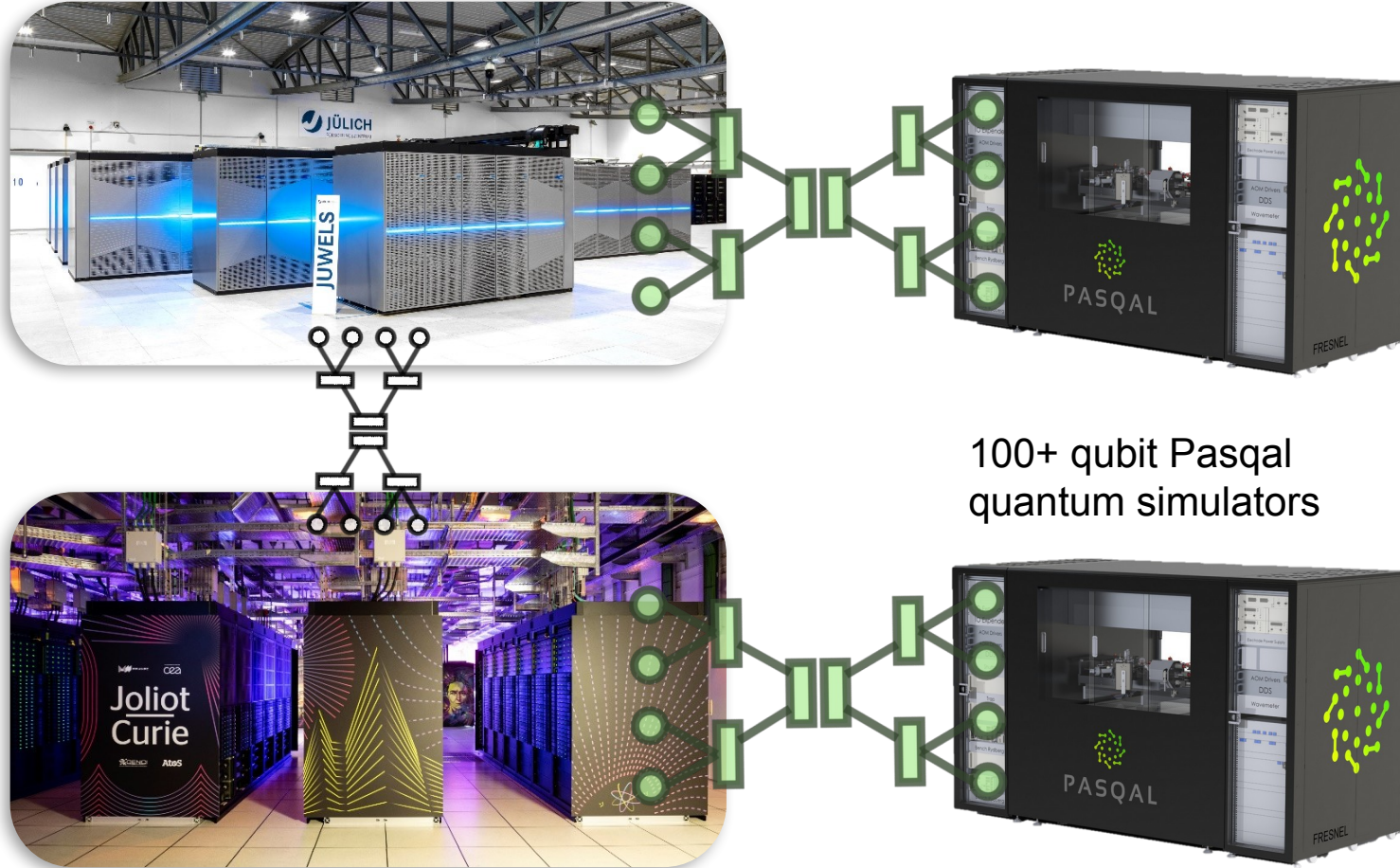
Why might this be interesting?

- discrete optimization is hard (NP-hard)
- produces many solutions “simultaneously”
- low energy consumption (compared to HPC)

JUNIQ Roadmap



High Performance Computer and Quantum Simulator hybrid



EuroHPC
Joint Undertaking

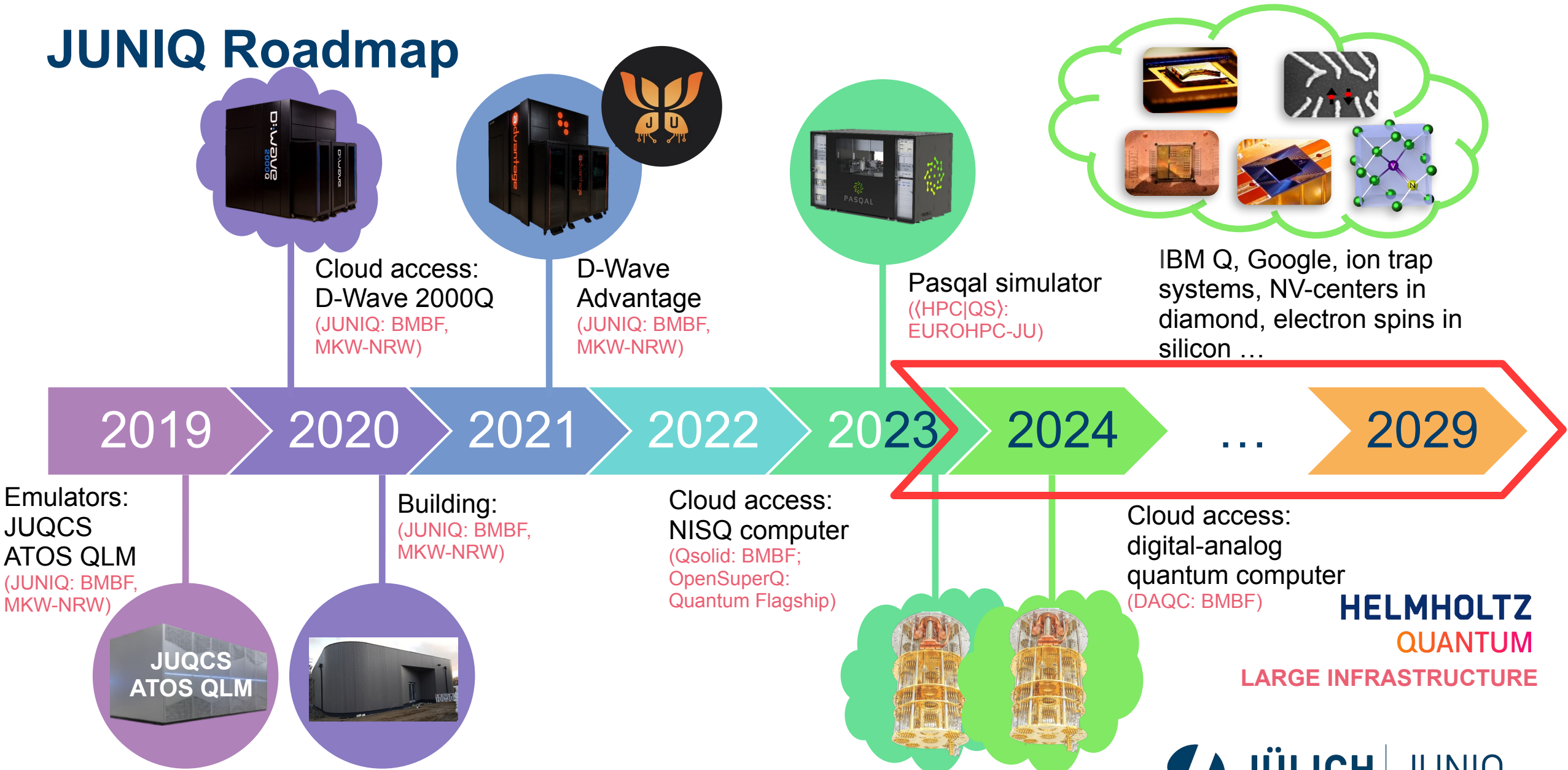
Support for the first hybrid HPC /
Quantum Computing
infrastructure in Europe

December 1, 2021

November 30, 2025



JUNIQ Roadmap



Hybrid Usage of High Performance & Quantum Computers

High Performance
Computers

Quantum Computers,
Simulators
& Annealers



Successful development
of quantum computing
applications

Hybrid Usage of High Performance & Quantum Computers

High Performance
Computers

Quantum Computers,
Simulators
& Annealers

**Thank you
for your attention**



Successful development
of quantum computing
applications