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The φ Josephson junction has a doubly degenerate ground state with the Josephson phases $\pm \varphi$. We demonstrate the use of such a φ Josephson junction as a memory cell (classical bit), where writing is done by applying a magnetic field and reading by applying a bias current. In the "store" state, the junction does not require any bias or magnetic field, but just needs to stay cooled for permanent storage of the logical bit. Straightforward integration with rapid single flux quantum logic is possible. © 2013 AIP Publishing LLC. [http://dx.doi.org/10.1063/1.4811752]

Superconducting digital circuits, in particular, based on Josephson junctions (JJs), potentially offer much lower energy dissipation per logical operation than the existing semiconducting devices. However, any practical computer as well as many digital devices need compatible superconducting random access memory (RAM). The largest superconducting RAM was demonstrated more than a decade ago and had a capacity of 256 kwords (1 word \equiv 16 bits). Its operation frequency of \sim 1 GHz is already similar to the one of modern computers, and a huge feed current of about 2 A was necessary for operation.

Since the beginning of this century, the JJ technology progressed significantly. For example, it became possible to obtain JJs with a phase difference of π in the ground state^{3–7}—the π junctions. φ JJs⁸ consisting of 0 and π parts connected together were proposed⁹ and demonstrated experimentally¹⁰ recently. Such a JJ has a doubly degenerate ground state with the Josephson phase $\psi=\pm\varphi$, where the value of $0<\varphi<\pi$ depends on design parameters. This results from the peculiar Josephson energy profile $U_J(\psi)$, which has a form of a 2π periodic double well potential. One can use these two intrinsic states of the φ JJ to store one bit of information.

In this paper, we demonstrate the operation of a memory cell (1 bit) based on a φ JJ—the so-called " φ bit," which is the first step towards a φ Josephson memory.

For the demonstration of the memory cell, we have used the same samples as in Ref. 10. Namely, the sample is a Nb|Al-AlO_x|Ni_{0.6}Cu_{0.4}|Nb multilayered heterostructure, with a step-like thickness of the ferromagnetic layer to obtain a 0- π JJ of moderate length. The sample fabrication is published elsewhere. ^{11,12} For this work, we have chosen a 0- π JJ with an in-plane length $L=200\,\mu\mathrm{m}$ and the width $W=10\,\mu\mathrm{m}$. By treating the structure as a whole (like a black box with two electrodes), one deals with a φ JJ. ^{9,10,13}

The current-voltage characteristic V(I) as well as the dependence of the critical current I_c on applied in-plane

magnetic field H are shown in Fig. 1. Already here one can see that we observe two different critical currents, I_{c-} or I_{c+} , depending on the initial state of the JJ, $\psi = -\varphi$ or $\psi = +\varphi$, as demonstrated in the original work. By applying a magnetic field, one can remove this degeneracy and observe only one branch on both V(I) (not shown) and on $I_c(H)$. A point-symmetric $I_c(H)$ with shifted cross-like minimum around zero field, as in Fig. 1(b), is the signature of a φ JJ.

For writing the state, we use the fact that an applied magnetic field changes the asymmetry between the two wells in the Josephson energy $U(\psi)$ up to the point that one of the wells disappears, ^{9,10} see Fig. 2(a). Hence one can set (write in) the desired state: $-\varphi$ for $H > H_1$ and $+\varphi$ for $H < -H_1$, where $\pm H_1$ is the field, at which one minimum in $U(\psi)$ disappears. After the state is written, one can return the magnetic field back to zero, and the state of the junction will be stored for infinitely long time provided the energy barrier is considerably larger than the energy of thermal or quantum fluctuations.

For reading out the state, we use the fact that the critical current of the φ JJ is equal to I_{c+} or I_{c-} depending on the initial state of the JJ, $+\varphi$ or $-\varphi$, see Fig. 2(b) as well as V(I) characteristics shown in Fig. 1(a). In our case, $I_{c+}\approx 480~\mu\text{A}$ and $I_{c-}\approx 370~\mu\text{A}$. Therefore, for the readout, we apply a bias current $I_{RO}=426~\mu\text{A}$ shown in Fig. 1(a), which lays in between I_{c+} and I_{c-} . Then, if the φ JJ switches to a voltage state, its critical current was $I_{c-} < I_{RO}$, i.e., the JJ was in the $-\varphi$ state. If, on the other hand, after the application of I_{RO} the JJ stays in a zero voltage state, then its critical current is $I_{c+} > I_{RO}$, i.e., its is in the $+\varphi$ state, see Fig. 2(b).

Note, that this readout technique is destructive, at least when we detect the $-\varphi$ state. After switching to a finite-voltage state, resetting the readout bias current back to zero, the phase may be retrapped either in the $-\varphi$ or in the $+\varphi$ state, depending on the JJ parameters such as on the damping. This retrapping of the phase is investigated in detail elsewhere. ¹⁴

In Figs. 2(c)–2(e), we demonstrate an experimentally recorded sequence of writing and reading $\pm \varphi$ states in different order. We start from an unknown state by applying a

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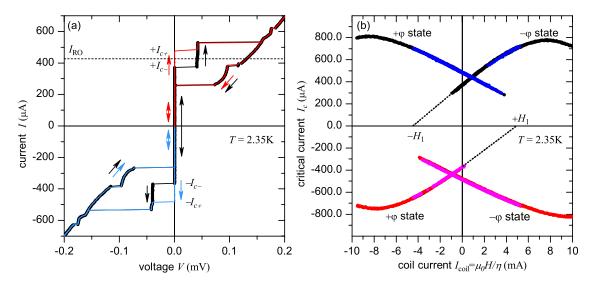


FIG. 1. (a) V(I) characteristic at zero field and (b) $I_c(H)$ curve of the investigated φ JJ measured at T = 2.35 K (overlap of several measurements with different initial conditions to trace out different branches).

positive magnetic field pulse, see Fig. 2(c), which removes the $+\varphi$ well, see Fig. 2(a), so that the phase relaxes into the $-\varphi$ well. After such a "writing" operation, the magnetic field is reset to zero, Fig. 2(c). Then, we apply a readout pulse, see Fig. 2(d) and observe a non-zero voltage across the junction, see Fig. 2(e), indicating a readout of the $-\varphi$ state. During the next write-read cycle, we apply the negative magnetic field H < 0, see Fig. 2(c), to change the asymmetry in the opposite direction, see Fig. 2(a), and write the $+\varphi$ state. After application of the readout pulse, see Fig. 2(d), we detect no voltage across the junction, see Fig. 2(e), indicating $a + \varphi$ state indeed. Further, the write-read cycles are repeated for the states $-\varphi, -\varphi, +\varphi, +\varphi, -\varphi, -\varphi, -\varphi, +\varphi, +\varphi, +\varphi$. One can see that the φ bit performs perfectly, reproducing the written sequence. Different longer sequences were played out as well without problems.

We have also investigated the margins of operation of our φ bit. The readout current I_{RO} should be well in between I_{c-} and I_{c+} , i.e., $|I_{RO}-I_{c\pm}|\gg I_{\text{noise}}$ —the background noise (electronic or thermal) in the bias current circuitry. In our case, $I_{\text{noise}}\sim 3\mu\text{A}$, so we have very large margins. The margins in terms of the magnetic field were determined experimentally. We have started seeing faults in operation when the writing magnetic field was decreased down to $|\mu_0 H_{\text{write}}^{\text{min}}|\approx 1.9\,\text{mA}\times\eta$ or increased up to $|\mu_0 H_{\text{write}}^{\text{max}}|\approx 2.1\,\text{mA}\times\eta$, where $\eta\sim 5\,\mu\text{T/mA}$ is the coil factor.

We have demonstrated the use of a φ Josephson junction as a memory cell (classical bit). The advantage of such a memory cell is that it is easily controllable by an applied magnetic field. The destructive readout can be performed as described here. However in real circuits, e.g., Rapid Single Flux Quantum (RSFQ) logic, one can construct novel cells that will perform readout with an output in a form of a single RSFQ pulse (or no pulse). In this case, the temperature limitation $T < 2.4\,\mathrm{K}$ necessary in the presented readout scheme to avoid the retrapping of the phase in a $+\varphi$ well upon escape from the $-\varphi$ well, see Fig. 2(b), is not relevant. By increasing the characteristic voltage V_c , one reduces the damping and facilitates operation at $T = 4.2\,\mathrm{K}$. This can be done, e.g., by using a superconductor with the larger energy

gap such as NbN. Besides this, λ_L for NbN is larger than for Nb, i.e., λ_J will be smaller. In fact, if used in RSFQ circuits, a φ JJ is a ready-to-use flip-flop.

In terms of the speed of the operation, the measurements presented in Fig. 2(c)-2(e) are performed on a 10 ms scale (delay time between setting the field/current and reading the voltage). In our case, the field is supplied by a coil, which is very slow and limits the writing time by ~ 5 ms. Applying the field by an on-chip control line will substantially accelerate the writing cycle. The most effective can be a control line passing through the upper or lower electrode of the JJ. Note also that the writing time is not limited by the remagnetization (domain flipping or rotation) of the ferromagnetic layer, like in some alternative superconducting memory proposals, 15 but rather by a characteristic frequency ω_c (damping) of the φ JJ. For the device presented here, $V_c \sim 150 \,\mu\text{V}$, i.e., $\omega_c/2\pi \sim 75$ GHz. In addition, we have a zero-field step at the voltage $V_1 \approx 50 \,\mu\text{V} \propto 1/L$, which appears only sometimes. If one takes shorter JJs, the step will appear at much larger voltages or will disappear, i.e., become irrelevant for $V_1 > V_c$.

In terms of miniaturization, the φ JJ used in this proof-ofprinciple experiment is rather large ($L = 200 \,\mu\text{m}$). However, already for existing technology, one can easily reduce its length down to $40-50 \, \mu \text{m}$. Further decrease of the size becomes difficult because the margins for the realization of a φ junction become very narrow. To realize a φ JJ on the basis of a $0-\pi$ JJ, one has to keep a certain relation between the lengths and critical currents of the 0 and π parts.^{9,13,16,17} If these parts have a normalized length $\sim \lambda_J$ and larger, then these requirements are quite relaxed, i.e., even 30% of asymmetry still results in a φ JJ. On the other hand, if the length of each part is shorter than $\sim \lambda_J$ then these requirements are rather strict (asymmetry should be controlled on a level better than 3%) and difficult to meet technologically. The way out of this problem is the further development of Superconductor-Insulator-Ferromagnet-Superconductor (SIFS) $0-\pi$ junction technology with the aim to increase $j_{c,\pi}$ from the present value of 40 A/cm² up to, say 4 kA/cm² (2 orders of magnitude). This can be done either by using a clean F layer or by using a

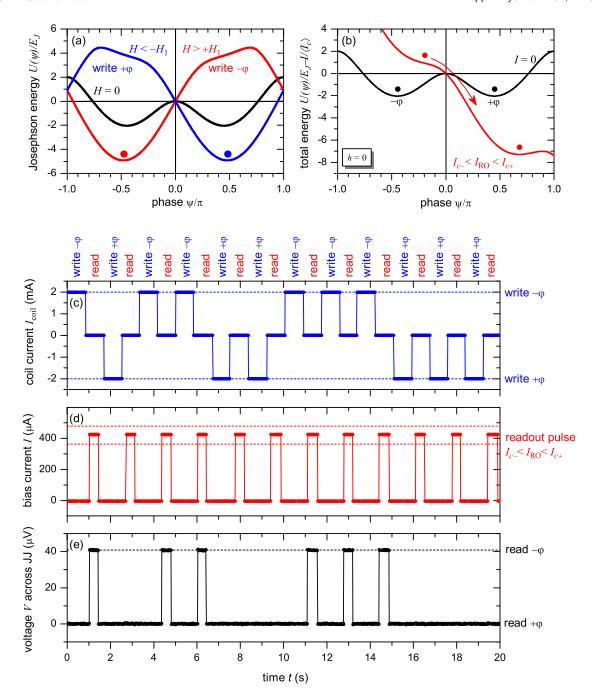


FIG. 2. (a) The principle of writing the state $+\varphi$ or $-\varphi$ by applying a magnetic field. (b) The principle of reading the state by applying the bias current $I_{c-} < I_{RO} < I_{c+}$. (c)–(e) The sequence of the external magnetic field (c), readout by the bias current (d) and the measured voltage (e), demonstrating the operation of the φ JJ as a memory cell. The speed of operation is limited by our dc setup (coil) and electronics.

SISIF structure. ¹⁸ In this case, $\lambda_J \propto 1/\sqrt{j_c}$ will become 10 times smaller, so that one can have a 4 μ m large bit. Another possibility to reduce λ_J is by increasing the specific inductance of the JJ electrodes

$$\mu_0 d' \approx \lambda_L \coth\left(\frac{d_1}{\lambda_L}\right) + \lambda_L \coth\left(\frac{d_2}{\lambda_L}\right).$$
 (1)

This can be done (a) by increasing λ_L , e.g., by using NbN electrodes; or (b) by decreasing the thicknesses d_1 and d_2 of the superconducting electrodes.

Alternative φ JJ technologies, ^{19–21} especially those providing intrinsic φ JJs²² may, at the end, lead to an extremely high-density memory.

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