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Biosensing near the neutrality point of graphene

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Over the past decade, the richness of electronic properties of graphene has attracted enormous interest for electrically detecting chemical and biological species using this two-dimensional material. However, the creation of practical graphene electronic sensors greatly depends on our ability to understand and maintain a low level of electronic noise, the fundamental reason limiting the sensor resolution. Conventionally, to reach the largest sensing response, graphene transistors are operated at the point of maximum transconductance, where 1/f noise is found to be unfavorably high and poses a major limitation in any attempt to further improve the device sensitivity. We show that operating a graphene transistor in an ambipolar mode near its neutrality point can markedly reduce the 1/f noise in graphene. Remarkably, our data reveal that this reduction in the electronic noise is achieved with uncompromised sensing response of the graphene chips and thus significantly improving the signal-to-noise ratio—compared to that of a conventionally operated graphene transistor for conductance measurement. As a proof-of-concept demonstration of the usage of the aforementioned new sensing scheme to a broader range of biochemical sensing applications, we selected an HIV-related DNA hybridization as the test bed and achieved detections at picomolar concentrations.

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INTRODUCTION

The concept of an ion-sensitive field-effect transistor (ISFET) (1,2) enables label-free detection of charged molecules on a small footprint upon its binding at the sensor surface, because it modulates the electrical current in the semiconductor channel due to field effect. Recent research trends now offer new opportunities for developing the modern version of a classical ISFET using graphene (and other two-dimensional materials) (3-11), with demonstrated greater sensitivity than traditional bioassays (12). Conventionally, the highest sensing response is reached when the graphene ISFET (GISFET or GFET) is operated at its maximum transconductance, which shows the largest change in the transistor current induced by a small change in the gate voltage. However, at the point of maximum transconductance, the electronic noise is found to be unfavorably large and therefore poses a major limitation to achieve next-generation graphene biochemical sensors with ever-demanding sensitivity (13-15).

Generally, the ubiquitous 1/f noise, whose power spectral density (PSD) inversely depends on the frequency f, dominates the noise spectrum of GFETs and determines its detection resolution at biologically relevant low frequencies ($\lesssim 1000~{\rm Hz}$) (13). Earlier studies also determined that, for graphene supported on a SiO₂/Si substrate, the background electrical noise is at minimum near the neutrality point of graphene where the electron density of states is lowest (16). In previous studies of graphene sensors in Hall geometry, biasing at this low-noise neutrality point can be favorably designed into the devices with the steepest sensing response in Hall resistivity (17, 18). Unfortunately, these works require an elaborate magnet setup, which is not suitable for integration and portable application. Here, we report an example of graphene chips operated near the low-noise neutrality point in simple transistor geometry (Fig. 1, A and B), without compromising any prospects of a label-free and portable graphene electronic sensor.

RESULTS AND DISCUSSION

Device preparation: In situ electrochemical cleaning for graphene surface refreshment

We investigated altogether 16 GFET devices prepared by transfer of chemical vapor deposition (CVD) graphene on three different substrates (7 devices on SiO₂/Si, 4 devices on Si₃N₄/SiO₂/Si, and 5 devices on sapphire; see Materials and Methods). Figure 1C shows the scheme of a conventional electrolyte-gated GFET. In Fig. 1D, the transfer curve in gray depicts a typical measured sheet conductance G of graphene plotted against the liquid-gate voltage V_{ref} (defined via an Ag/AgCl reference electrode) for an as-fabricated device on a SiO₂/Si substrate (GFET-I). Despite the fact that we treated the SiO₂ with hexamethyldisilazane (HMDS) (19) (before graphene transfer) to effectively shield the graphene from trapped charges on the SiO₂ surface, it was common that we observed (Fig. 1D, gray curve) multiple neutrality points (at $V_{\rm NP} = -0.11$, 0.11, and 0.39 V) and relatively large hysteresis (~50 to 100 mV). These poor device performances against liquid-gate voltage sweeping suggest significant charged trap states at the graphene/ electrolyte interface, an indicator of the presence of a large amount of surface contaminants (even though all the devices were baked at ~200°C and thoroughly rinsed in isopropanol; see Materials and Methods) (20). To restore highly reliable device characteristics before measurement, the graphene transistor is subjected to an in situ electrochemical cleaning that rapidly removes any surface contaminants from graphene (see Materials and Methods) (21–24). This electrochemical cleaning technique yields consecutively recovered transfer curves of the GFET-I as shown in Fig. 1D (upper panel, sheet conductance mapping), suggesting a surface refreshment of graphene. Every consecutive cleaning cycle decreases the hysteresis and removes the spurious neutrality points observed at gate voltages of -0.11 and 0.39 V. After 10 cycles of refreshment (red line, Fig. 1D), the $G(V_{ref})$ curve of the GFET-I became completely stable, and we were able to eliminate both the initial hysteresis and the spurious neutrality points observed at $V_{\text{ref}} = -0.11$ and 0.39 V. Further cycling at an operational window of (-0.4 V, 0.6 V) results neither in any shift of the neutrality point nor in any change of the GFETs' conductance. Using an interface capacitance of ~2 μF/cm² (7), we estimate the field-effect mobility of this electrolyte-gated GFET-I to be ~1100 cm²/Vs for both hole and electron carriers. This

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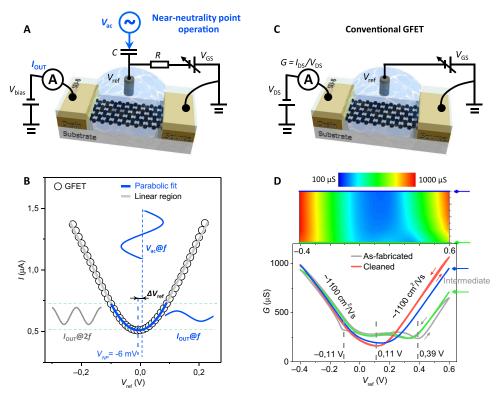


Fig. 1. Near–neutrality point operation and conventional GFET. (**A**) Schematic presentation of near–neutrality point operation of an electrolyte-gated GFET device. (**B**) The transfer curve $I(V_{ref})$ of the GFET-II (black circles, after electrochemical cleaning) and the corresponding parabolic fitting (blue line) around the neutrality point and the linear fit (gray lines) away from the neutrality point. The working principle of the electrolyte-gated GFET operated near the neutrality point is also illustrated in the inset diagram: The output current contains both the second harmonic component $0.5a_0A_{ac}^2\cos 4\pi ft$ (I_{OUT} at frequency I_{OUT}) and the fundamental component (I_{OUT} at frequency I_{OUT}) at frequency I_{OUT} at f

mobility number is in good agreement with the field-effect mobility of ~ 1000 to $1500 \, \mathrm{cm^2/Vs}$ that we obtained using a Si back gate before the electrochemical surface refreshment and liquid gating, indicating that the underlying Si substrate plays an important role in determining the electrical properties of our GFET devices (11).

Graphene transistors operated near the neutrality point with minimum 1/f noise

Owing to the lack of an intrinsic band gap (25, 26), the GFETs present typical ambipolar transfer characteristics without an off-state (for example, Fig. 1D; red line). The charge carriers in the graphene channel can be continuously tuned from holes to electrons when sweeping the liquid-gate voltage from negative to positive. At the transition point (0.11 V), which is the so-called charge neutrality point with nearly equal electron and hole densities, the graphene conductance G reaches its minimum value. As a procedure routinely used in many studies, the gate voltage shifts of the $G(V_{ref})$ curves are deduced upon addition of analytes to evaluate the sensing response of a GFET device. However, this procedure cannot be adopted at the neutrality point (where the 1/f noise in graphene is optimal) because the transconductance and thus its related sensing response are (close to) zero at this region. Alternatively, here we report an elegant and simple approach permitting low-noise operation near the neutrality point of graphene by harvesting its unique ambipolar behavior: We apply a sine wave

to cycle the gate voltage of an electrolyte-gated GFET (GFET-II) around its neutrality point and to monitor the output current in a common-source configuration. As illustrated in Fig. 1A, an ac drive voltage, $V_{\rm ac}$, with a typical amplitude of 70.7 mV (ranging from 14.1 to 282.8 mV) and a frequency f of 77.77 Hz (ranging from 9.111 to 2.161 kHz), was provided by an SR830 lock-in amplifier (Stanford Research Systems) and delivered to the liquid gate of the GFET. The dc bias drain-source voltage $V_{\rm bias}$ and dc liquid-gate voltage $V_{\rm GS} = V_{\rm NP} + \Delta V_{\rm ref}$ were maintained using homemade battery-based voltage sources, and the rectified output drain current was monitored using the lock-in amplifier.

The nonlinearity of the symmetric $I(V_{\rm ref})$ curve (black circles, Fig. 1B) near the neutrality point plays a key role in the applications. As shown by the fitted blue line (Fig. 1B), we can approximate the $I(V_{\rm ref})$ relation of the GFET around the neutrality point by using a parabolic function: $I = a_0(V_{\rm ref} - V_{\rm NP})^2 + b_0$, with a_0 and b_0 denoting the two fitting parameters. If we configure the Ag/AgCl reference electrode with a dc gate voltage close to $V_{\rm NP} = -6$ mV modulated with a single-tone sinusoidal voltage $V_{\rm ac} = A_{\rm ac} \sin(2\pi ft)$ (sinusoidal wave in blue, $V_{\rm ac}$ in the inset of Fig. 1B), ideally, the corresponding rectified output current can be described as

$$I_{\text{OUT}} = I_{\text{dc}} - 0.5a_0 A_{\text{ac}}^2 \cos 2\pi (2f)t + 2a_0 A_{\text{ac}} \Delta V_{\text{ref}} \sin 2\pi ft$$
 (1)

where $I_{dc} = b_0 + a_0 \Delta V_{ref}^2 + 0.5 a_0 A_{ac}^2$ is the dc component of the output current, and $\Delta V_{\rm ref}$ is the small gate voltage biased away from the neutrality point. It is clear that the output current contains both the second harmonic (at frequency 2f) and the fundamental component (at frequency f), as illustrated by the gray $(I_{OUT}@2f)$ and the blue $(I_{OUT}@f)$ sinusoidal waves in the inset of Fig. 1B, respectively. The output at frequency 2f is a constant signal once the amplitude of the input sine-wave $A_{\rm ac}$ is fixed. In this way, one can realize a frequency-doubling device with just a single graphene transistor that gives a high-purity output spectrum (more than 90% of the total output energy) without any additional filtering, as already confirmed by our previous study (7). However, in contrast to previous work, here we concentrate on the output signal at frequency f, the magnitude of which is minimized (close to zero) at the neutrality point but raises proportionally to $\Delta V_{\rm ref}$ (Eq. 1) with a prefactor $2a_0A_{ac}$. Next, we will first characterize the low-frequency noise in GFET, after which we will focus on monitoring the rectified output drain current ($I_{OUT}@f$, in response to either a step gate voltage or single-stranded DNA (ssDNA) analytes as will be shown in Figs. 3 and 4, respectively) and show that operating the GFET near its neutrality point can markedly reduce the 1/f noise in graphene.

In Fig. 2A, we characterized and plotted the PSD (see Materials and Methods) S_V of the GFET-I against the liquid-gate voltage $V_{\rm ref}$ after electrochemical cleaning. All the curves exhibit 1/f dependence, as indicated by the dashed gray line. We tested also the S_V at various drain-source voltage drops $V_{\rm DS}$ and found $S_V \simeq V_{\rm DS}^2/f$ (fig. S3), suggesting a clear 1/f behavior according to Hooge's empirical law (13). The low-frequency noise of the GFET exhibited a 1/f behavior, regardless of whether the measurements were performed in air or in electrolyte solution under different ionic strengths (fig. S3). In previous works, 1/f noise has been advantageously explored for realizing graphene-selective gas sensors (27, 28). The vapors of different chemicals produce distinct effects on the low-frequency noise spectra of single pristine graphene transistors, forming unique gas signatures without specific graphene surface functionalization. This sensing mechanism also holds potential for other two-dimensional materials (29) and calls for future exploration.

For a more clear comparison, we plot the normalized PSD S_V/V_{DS}^2 (at f = 10 Hz with a bandwidth of 1 Hz) of the refreshed GFET-I as a function of the liquid-gate voltage $V_{\rm ref}$ in Fig. 2B (black circles). Notably, the high resistance at the neutrality point around $V_{ref} = 0.1 \text{ V}$ leads to a high source-drain voltage $V_{\rm DS}$ (in a constant-current configuration; refer also to Materials and Methods), and therefore a minimum S_V/V_{DS}^2 even if the $V_{\text{ref}} = 0.1 \text{ V}$ gives rise to the largest S_V (green line, Fig. 2A). After systematically investigating the 1/f noise behavior of graphene devices on different substrates (SiO₂, Si₃N₄, and sapphire), we conclude that the 1/f noise in graphene is always (local) minimum at and increases with carrier concentration around its neutrality point, revealing a "V"- or "M"-shaped feature regardless of the substrates (Fig. 2, B and C, and fig. S4), in agreement with previous reports (13). The typical channel area-normalized PSDs that we achieved in this study $(\sim 2 \times 10^{-8} \text{ to } 4 \times 10^{-7} \, \mu\text{m}^2/\text{Hz} \text{ at } f = 10 \, \text{Hz on SiO}_2/\text{Si})$ are comparable to the previously reported PSD level of $\sim 10^{-8}$ to 10^{-7} $\mu m^2/Hz$ for micrometer-scale graphene devices on SiO₂/Si substrate (13). For graphene devices fabricated on Si₃N₄ substrates, we observed channel area-normalized PSDs on the order of $\sim 1 \times 10^{-8} \, \mu \text{m}^2/\text{Hz}$ (fig. S4A) after surface refreshment, which are comparable or even superior to previously reported very low noise of suspended ($\sim 0.5 \times 10^{-8} \, \mu \text{m}^2/\text{Hz}$) (14) or h-BN-encapsulated ($\sim 0.5 \times 10^{-8}$ to 3×10^{-8} µm²/Hz) (15) graphene, making our refreshed GFETs ideal candidates for low-noise electronic biosensors.

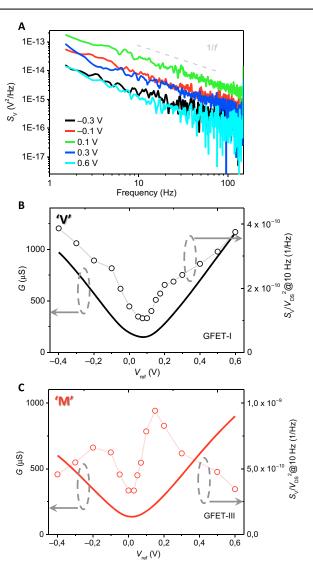


Fig. 2. 1/f noise performance of electrolyte-gated GFET devices. (A) PSD $S_V(f)$ of GFET-I at different liquid-gate voltages $V_{\rm ref} = -0.3$, -0.1, 0.1, 0.3, and 0.6 V tested immediately after an initial cleaning. (B) $G(V_{\rm ref})$ curves (to the left axis) and the corresponding "V"-shaped normalized PSD $S_V/V_{\rm DS}^2$ (at f=10 Hz with a bandwidth of 1 Hz, to the right axis) for the GFET-I after moderate electrochemical cleaning. (C) $G(V_{\rm ref})$ curves (to the left axis) and the corresponding "M"-shaped normalized PSD $S_V/V_{\rm SD}^2$ (at f=10 Hz with a bandwidth of 1 Hz, to the right axis) for another GFET-III (also fabricated on a SiO $_2$ /Si substrate) after moderate electrochemical cleaning.

Next, we operated the GFET near its neutrality point (Fig. 1A) and recorded the changes of the output current ΔI_f in steps of $\Delta V_{\rm ref} = 200~\mu{\rm V}$. At the same time, we varied the input sine-wave $A_{\rm ac}$ to find the most suitable value. When the amplitude of the input sine-wave $A_{\rm ac}$ increased from 14.1 to 70.7 mV, the sensing response (given by $\Delta I_f/200~\mu{\rm V}$) increased linearly from 0.85 to 4.25 $\mu{\rm S}$ (and from 0.17 to 0.85 nA in ΔI_f ; red bars, Fig. 3A). The linear behavior (dashed line in red, Fig. 3A) agrees with our proposed model ($\Delta I_f = 2a_0A_{\rm ac}\Delta V_{\rm ref}$; see Eq. 1). The deduced fitting parameter $a_0 = 30~\mu{\rm S/V}$ quantitatively agrees with that (29 $\mu{\rm S/V}$) extracted from the parabolic fitting in Fig. 1B. As $A_{\rm ac}$ keeps increasing into the linear region (141.4 and 282.8 mV; gray lines, Fig. 1B), the sensing response keeps increasing but at a lower rate, because now Eq. 1 is no longer fully valid. In particular, the marked increase

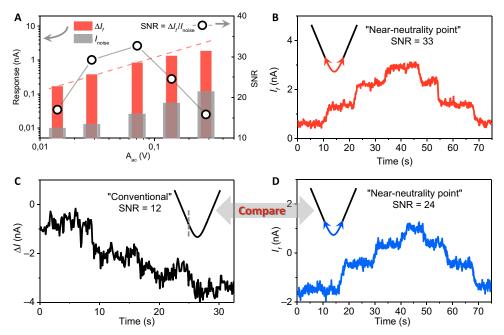


Fig. 3. Low-noise graphene transistors operated near the neutrality point. (A) Measured sensing response (red bars), root mean square (RMS) current noise level (gray bars), and the corresponding SNR (black circles) close to the neutrality point as a function of the amplitude A_{ac} of the gate voltage swing. The sensing current was monitored in response to a 200- μ V gate voltage change after surface refreshment with a moderate electrolysis window (-0.4 V, 0.8 V). A maximum SNR of 33 was achieved at $A_{in} = 70.7$ mV. Its corresponding responses to a 200- μ V step gate voltage change versus time is shown in (B). (C and D) Comparison of the GFET-II in response to the 200- μ V step gate voltage change when operated in conventional mode and near the neutrality point, respectively.

in the current noise I_{noise} (measured by using a SR830 lock-in amplifier, at 1 Hz bandwidth) leads to a significantly decreased signal-to-noise ratio (SNR) = $\Delta I_f/I_{\text{noise}}$ as illustrated in Fig. 3A (black circles). It is therefore beneficial to perform the sensing test at moderate input gate voltage amplitude ($A_{ac} = 70.7 \text{ mV}$ in this case), and the corresponding response to a 200-µV step gate voltage change is shown in Fig. 3B. In Fig. 3 (C and D), we compare the actual sensing response of the GFET-II [after electrochemical cleaning at (-0.4 V, 0.6 V) but before (-0.4 V, 0.8 V)] operated in a conventional mode and in an ambipolar mode near the neutrality point, respectively. For a clear comparison, we have maintained both sensing responses at ~4 µS by adjusting the applied drain-source voltage $V_{\rm DS}$ (Fig. 1C) or the bias voltage $V_{\rm bias}$ (Fig. 1A). In the conventional mode (Fig. 3C), we operate the GFET at the outmost point of the parabolic region with an optimal SNR (section S4) and a transconductance of 4.4 µS. For the near-neutrality point operation (Fig. 3D), with $A_{ac} = 70.7$, we found an I_{noise} as low as 0.036 nA, which outperforms that of the conventional configuration (0.073 nA) by more than a factor of 2. This impressive enhancement of the noise performance is accompanied by an excellent sensing response of 4.25 μ S, yielding an SNR of 24 in comparison to that of 12 tested in the optimized conventional configuration (Fig. 3C). We are convinced that our achievements in operating the GFET devices near the neutrality point with uncompromised sensing responses would significantly advance and extend the usage of low-noise graphene chips to a broader range of biochemical sensing applications and beyond, especially given the fact that our approaches are fully complementary to previously reported strategies for 1/f noise reduction (13–16). Notably, cycling the GFETs around the low-noise neutrality point can also be achieved using a back gate when exposing the devices to air (as gas sensors, for example).

Low-noise GFET biosensors

As a proof-of-principle demonstration, in this section, we apply highperformance GFET-II configured in low-noise mode near the neutrality point as potent DNA sensors. This device was cut from a 5×10 array of GFET devices with SU-8 liquid channel (Fig. 4A). After electrochemical cleaning, we first functionalize the surface of graphene with pyrene-linked peptide nucleic acid (pPNA) molecules 5'-AAGCTACTGGA-Lys (pyrene)-3' (a synthetic molecule in complementary to our target HIV virus-related ssDNA molecule; see Materials and Methods). Tween 20 was then introduced to self-assemble on the graphene surface to maximize biospecific binding and ruling out possible false nonspecific positives (Fig. 4B; see also Materials and Methods) (30). The self-assembly process was monitored and confirmed in a conventional measurement scheme (Fig. 4C). After surface functionalization, the GFET was flushed thoroughly with an excess of 1 mM phosphate-buffered saline (PBS) buffer solution to remove unbound molecules, yielding a graphene surface with firmly adsorbed pPNA and Tween 20 molecules (as illustrated in Fig. 4B) via π - π and hydrophobic interactions.

In the next step, we investigated the chemical response of the pPNA-functionalized graphene upon ssDNA molecule adsorption in real time when operated near its neutrality point. We first injected fully complementary ssDNA molecules with a concentration of 10 pM. When the complementary ssDNA molecules reach the liquid chamber, they diffuse to the graphene channel and account for the clear sensing signal in the inset of Fig. 4D. Under an RMS SNR of 1, we can extract a limit of detection of 2 pM, which is even better than that (4 pM) of our previously reported ultrasensitive DNA sensors (7). To confirm the specificity of our detection, we introduced 1-base mismatched ssDNA at the same concentrations of 10 pM, and no noticeable signal was observed (fig. S6). We also injected fully complementary ssDNA molecules with a

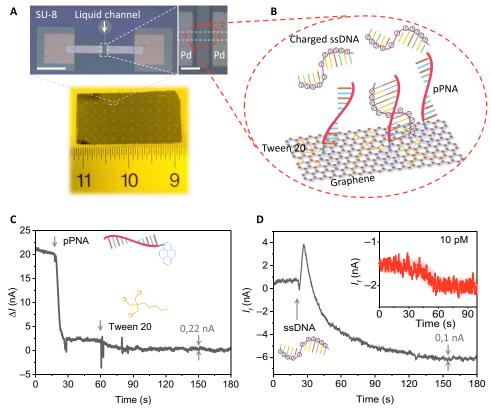


Fig. 4. DNA sensing near the neutrality point of graphene. (A) Upper panel: Optical micrographs of GFET-II. The left image shows the openings defined by a $1-\mu$ m-thick SU-8 photoresist. Scale bar, 200 μ m. The right image is a zoom of the SU-8 liquid channel across a graphene flake (white dashed lines in between the two Pd electrodes). Scale bar, 20 μ m. Lower panel: A 5×10 GFET device array fabricated on a SiO₂/Si substrate. (B) Schematic illustration of negatively charged complementary ssDNA molecules bind to pPNA molecules that are noncovalently anchored on the graphene surface. Nonspecific binding of biomolecules directly on the GFET was prevented by self-assembling Tween 20 on the graphene surface. (C) Changes in ΔI of the graphene sensor in a conventional GFET measurement scheme upon the self-assembly processes of pPNA and Tween 20 with concentrations of 1 μ M and 0.05 wt %, respectively. Current noise, 0.22 nA. (D) Changes in I_I of the graphene sensor operated near its neutrality point versus time upon the introduction of 1 nM and 10 pM (inset) fully complementary ssDNA. Current noise, 0.1 nA. All were tested in 1 mM PBS buffer solution.

concentration of 1 nM (as indicated by the initial spike upon ssDNA solution injection, which suggests a rapid response of the sensor). Its magnitude is 7 nA, sitting on a background noise of 0.1 nA, confirming our observed positive signal at 10 pM ssDNA. It is also clear that by cycling the ambipolar graphene transistor around its neutrality point, the current noise in Fig. 4D (0.1 nA) is significantly reduced compared to that (0.22 nA) in Fig. 4C operated in a conventional GFET measurement scheme (with similar sensing responses).

It is noteworthy that all our experiments are based on single-layer graphene (fig. S1; see also Materials and Methods). The 1/f noise in graphene depends on the number of layers. Double- or few-layer graphene devices are expected to reduce the 1/f noise (16, 31). We believe that the optimization of graphene biosensors calls for additional studies of the number of layers on both the 1/f noise level and the sensing response in liquid environments. This is because the band structure and electrical properties of few-layer graphene are different from those of single-layer graphene. Few-layer graphene devices would lose the steep I- V_{GS} curve observed for single-layer graphene (31), leading to significant reduction in the GFET amplification and degradation in the sensing response. In this regard, single-layer graphene with a large sensing response (as investigated in the current work) is likely optimal for graphene sensor application, as also advocated in the literature reporting single-layer graphene-based biosensors with superior performance (4, 11, 12). Nevertheless, the techniques that we proposed in this article deal with

the reduction of 1/f noise, which is dominated by surface over bulk noise in graphene up to seven layers (31). Thus, our noise reduction techniques for graphene sensor applications near the neutrality point also hold potential for few-layer devices.

CONCLUSION

We demonstrate that operating a graphene transistor in an ambipolar mode near its neutrality point can markedly reduce the 1/f noise in graphene. The development of low-noise, portable, and reliable graphene sensors for point-of-care applications is at the frontier of graphene electronics and biosensors and could have an enormous societal impact for the broader field of medical diagnosis. Along with the electrochemical surface refreshment technology introduced in this paper, we expect that our crucial improvements in device sensitivity and reliability of graphene electronics operated in a liquid environment will be important in this pursuit, as well as for new insights into the 1/f noise mechanisms in graphene.

MATERIALS AND METHODS

CVD graphene growth and transfer

Single-crystal monolayer graphene films (fig. S1) were grown by CVD using a gas mixture of Ar, H₂, and diluted CH₄ [200 parts per million

(ppm) CH₄ balanced in argon] on 25- μ m-thick copper foils (99.8%, Alfa Aesar, 13382). After washed by acetone and HCl/H₂O (1:20), the copper foils were thoroughly rinsed in deionized (DI) water and blow-dried (N₂). The cleaned copper foils were then loaded into a homebuilt CVD system and preannealed at 1050°C under a flow of 100-sccm (standard cubic centimeter per minute) Ar at 0.8 bar for 2 hours. Thereafter, single-crystal graphene was grown at 1050°C and 10 torr for 5 hours under a flow of 100-sccm Ar, 10-sccm hydrogen, and 20-sccm diluted CH₄. The growth was terminated by quenching the quartz tube in ambient environment (cooling rate, \sim 100°C/min).

The transfer of the CVD graphene films on the copper foils was carried out by first spin-coating a poly(methyl 2-methylpropenoate) (PMMA) film over the graphene film. After etching away the graphene coverage on the other side by O_2 plasma, the copper films were dissolved in an ammonium persulfate solution. The solution was exchanged with DI water for several times before the graphene/PMMA films were transferred onto various substrates. Eventually, the PMMA films could be dissolved using acetone, leaving uniform, large-area monolayer graphene on the substrate for further processing.

Device fabrication

The preparation of GFET devices on sapphire can be found elsewhere (4). The rest of the electrolyte-gated GFET devices were fabricated either on undoped Si substrates with ~100-nm dry SiO₂ or on undoped SiO₂/Si substrates with 100-nm plasma-enhanced CVD low-stress Si₃N₄. Both substrates were precoated with HMDS (19). These electrolytegated GFET devices were patterned and metalized (60-nm Pd) by using standard electron-beam lithography technique, resulting in transistors having a length of 20 µm and a width of 10 µm on a SiO₂/Si substrate and 10 μm by 10 μm on a Si₃N₄/SiO₂/Si substrate. As schematically sketched in Fig. 1 (A and C), the liquid handling was achieved via a sequential fabrication of (i) a PMMA or SU-8 layer to define the micrometer-sized liquid channel and after wire bonding and (ii) a biocompatible, two-component epoxy to seal the contact. Before electrical characterization, all the devices were baked (200°C) for at least 1 hour under vacuum or ambient conditions and thoroughly rinsed in isopropanol.

Electrostatic-assisted electrochemical refreshment of graphene surface

A mild cyclic liquid-gate voltage applied to a GFET can very rapidly remove its surface contaminants—introduced during either CVD graphene transfer or device fabrication/storage (see section S2). Here, a conservative electrochemical condition of (-0.4 V, 0.6 V) and a moderate condition of (-0.4 V, 0.8 V) were applied for Figs. 1D and 2 (C and D), respectively, at a sweeping rate of ~1 to 10 mV/s against an Ag/AgCl reference electrode when the graphene conductive channel was held at ground. As schemed in fig. S2, to further accelerate the electrochemical processes, a sine wave with $V_{\rm in} = 70.7$ mV with relatively high frequency (77.77 Hz) was superpositioned on the liquid gate. The oscillatory voltage imposed a periodic force on charged nanoparticles and impurities on or close to the graphene surface. Loosely adsorbed nanoparticles and impurities spread out and diffused away into the bulk of the buffer solution. In addition, the oscillatory voltage was promised to speed up the electrolytic reactions, and thus allowing an improved outcome, through facilitating and accelerating the diffusion of reactive agents. Notably, we expect that the electrochemical cleaning technique can be adopted to clean the surface of back-gated GFETs if followed by thorough rinsing and blow-drying.

Noise characterizations

For electronic noise characterization in general, a clean current source $I_{\rm DS}$ (homemade battery-based) was connected to the drain electrode, and the corresponding voltage drop over the graphene channel $V_{\rm DS}$ was monitored and analyzed by using a dynamic signal analyzer (HP35670a; see fig. S3A). In Fig. 3 (B to D), the background electronic noise was tested by using an SR830 lock-in amplifier (Stanford Research Systems). In Fig. 4 (C and D), the background noise was estimated from the SD of the data sets.

Noncovalent surface functionalization of graphene

The PNA molecules 5'-AAGCTACTGGA-Lys (pyrene)-3' is a synthetic molecule complementary to our target HIV virus-related ssDNA molecule (32) 5'-TCCAGTAGCTT-3' and its 1-base mismatched molecule 5'-TCCAGAAGCTT-3' (all purchased from Eurogentec S.A.). In Fig. 3B, Lys (pyrene) is a molecular linker group with a pyrene unit, which can be noncovalently anchored onto graphene surface via π - π interaction. To prevent nonspecific binding of biomolecules directly to the GFET, Tween 20 was then applied to self-assemble on the graphene surface in 1 mM PBS solution with 0.05 weight % (wt %) concentration. Tween 20 owns two important parts: an aliphatic chain that can immobilize on the hydrophobic graphene surface by noncovalent interaction, and aliphatic ester chains that can prevent nonspecific binding of biomolecules, thus maximizing biospecific binding to the surface-anchored recognition probes and ruling out possible false positives (30). We note here that the noise level of the device had a tendency to increase after the surface functionalization. We ascribe the slight increase in the electrical noise to the unbinding/free sites of the surface PNA molecules, which could, in principle, introduce trap states and current fluctuations, in accordance with previous reports (33).

SUPPLEMENTARY MATERIALS

Supplementary material for this article is available at http://advances.sciencemag.org/cgi/content/full/3/10/e1701247/DC1

section S1. Single-crystal monolayer CVD graphene

section S2. Electrochemical cleaning of graphene: Basic principle

section S3. Noise characterizations

section S4. SNR in conventional operated GFETs

section S5. pPNA-DNA hybridization: 1-base mismatched

fig. S1. High-quality single-crystal CVD graphene.

fig. S2. Schematic presentation of in situ electrochemical cleaning of an electrolyte-gated GFET device.

fig. S3. Electronic noise characterization for graphene on SiO₂/Si substrate.

fig. S4. Electronic noise characterization for graphene on Si_3N_4/Si and sapphire substrates.

fig. S5. SNR in conventional operated GFET devices.

fig. S6. No obvious changes in I_r of the graphene biosensor versus time upon the introduction of 10 pM 1-base–mismatched ssDNA in 1 mM PBS solution.

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